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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Rawlins et al.

Appl. No. 09/986,764

Filed: November 9, 2001

For:

Method and Apparatus for Reducing DC Offsets In A

Communication System

Confirmation No. 5623

Art Unit: 2631

Examiner: Vo, Don Nguyen

Atty. Docket: 1744.1330000

Letter to PTO Draftsman: Submission of Formal Drawings

Commissioner for Patents Washington, D.C. 20231

Sir:

Submitted herewith are eighty one sheets of formal drawings with Figures: <u>1A-1D</u>, <u>2, 3A-3G</u>, <u>4, 5, 6A, 6B, 7A, 7B, 8–12, 13A-13I, 14-23, 24A, 24B, 25A, 25B, 26-30, 31A, 31B, 32A, 32A-1, 32A-2, 32A-3, 32A-4, 32B, 32B-1, 32B-2, 32B-3, 33-52, corresponding to the informal drawings submitted with the above-captioned application. Identification of the drawings is provided in accordance with 37 C.F.R. § 1.84(c). Acknowledgment of the receipt, approval, and entry of these formal drawings into this application is respectfully requested.</u>

It is not believed that an extension of time is required, other than any already provided herewith. However, if an extension of time is needed to prevent abandonment of the application, then such extension of time is hereby petitioned. The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

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Michael Q. Lee

Attorney for Applicants Registration No. 35,239

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SKGF_DC1:110919.1



Sheet 1 of 64

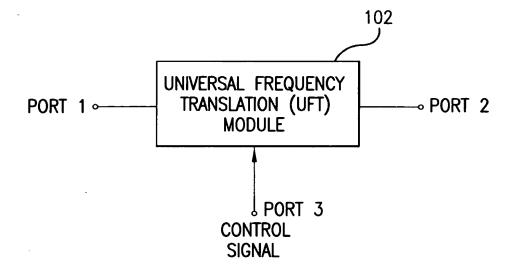


FIG. 1A

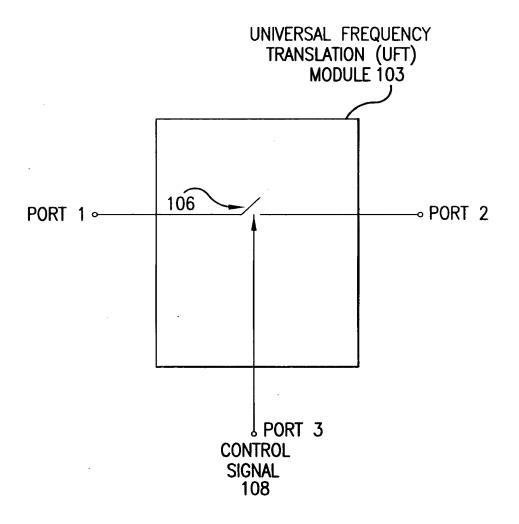


FIG. 1B

In a Communication System

UNIVERSAL FREQUENCY DOWN-CONVERSION (UFD) MODULE 114

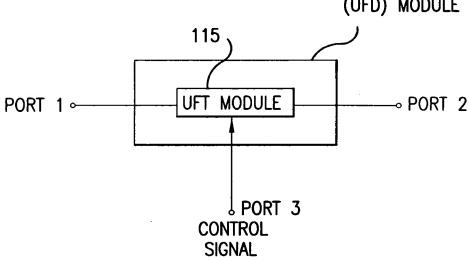


FIG. 1C

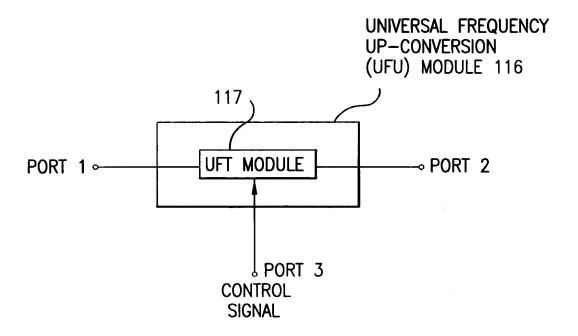


FIG. 1D



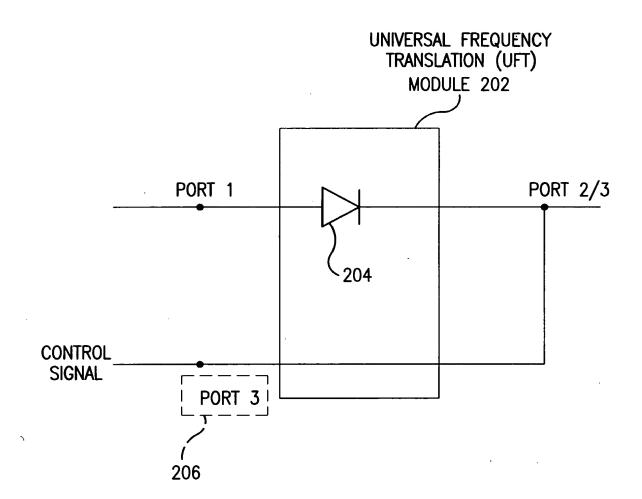


FIG. 2

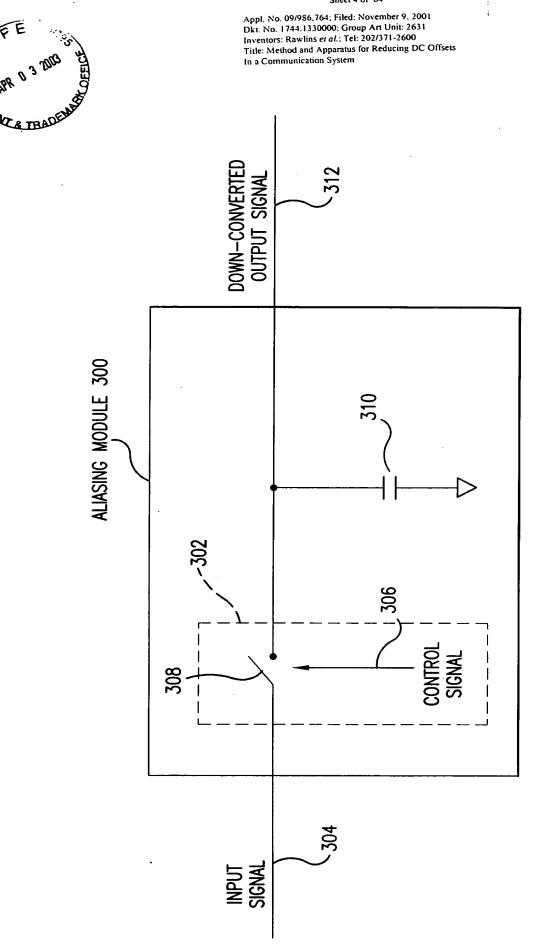
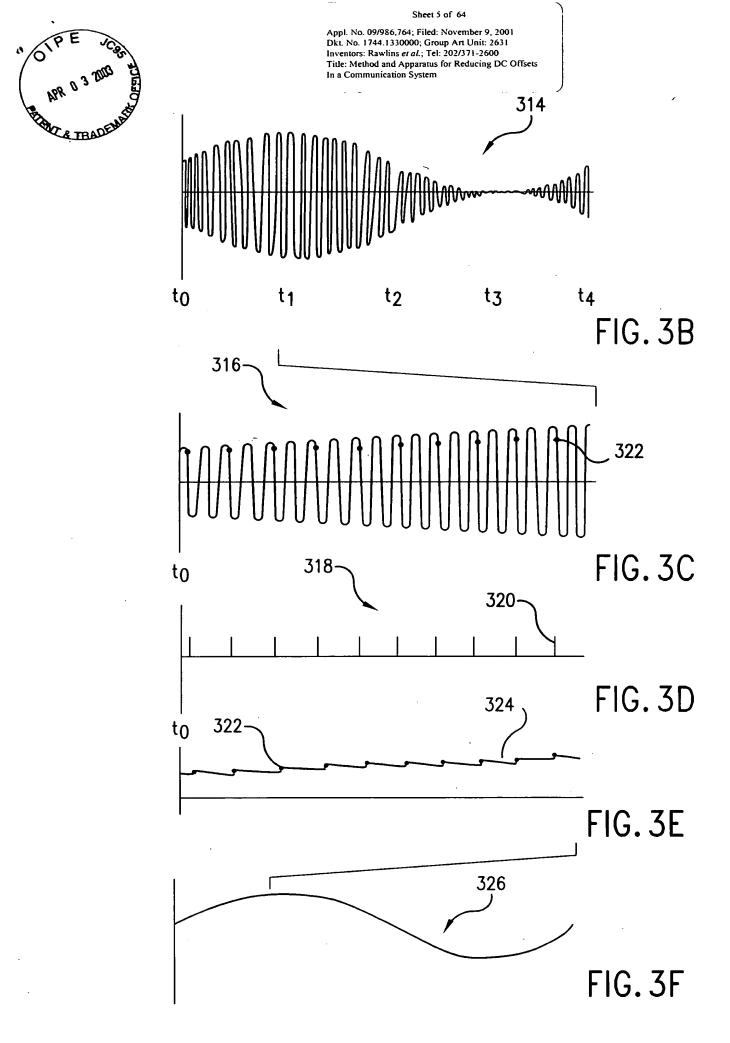


FIG. 3A

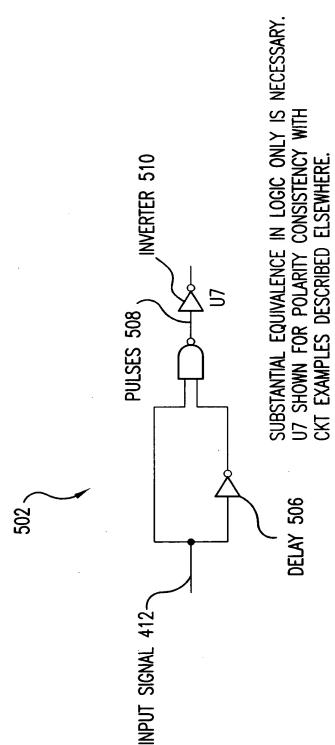


CONTROL SIGNAL

INPUT SIGNAL

ALIASING MODULE 300





F16.5



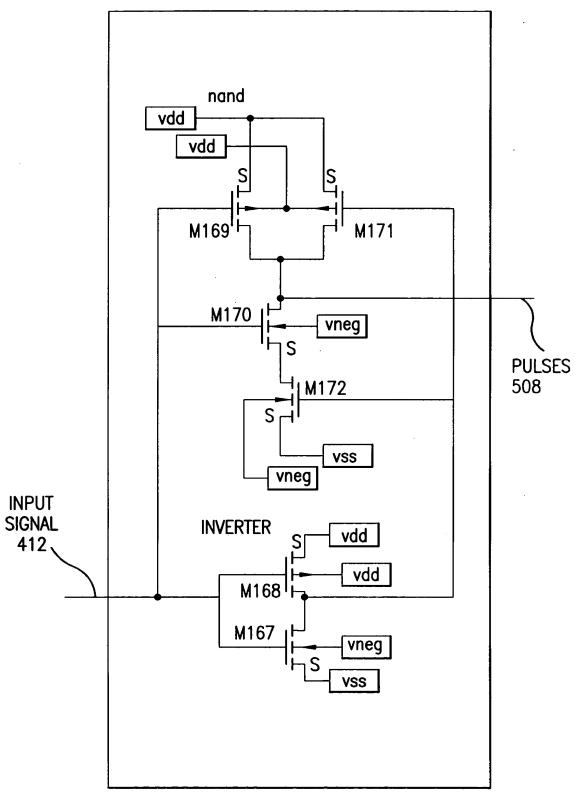


FIG. 6A



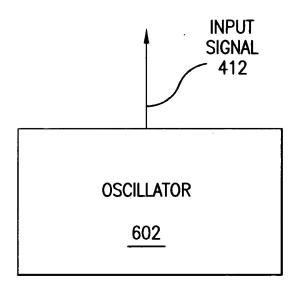


FIG. 6B



412 508

506 RISING EDGE PULSE GENERATOR

FIG. 7A

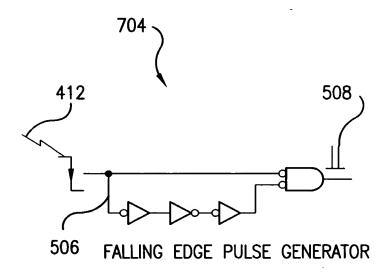


FIG. 7B



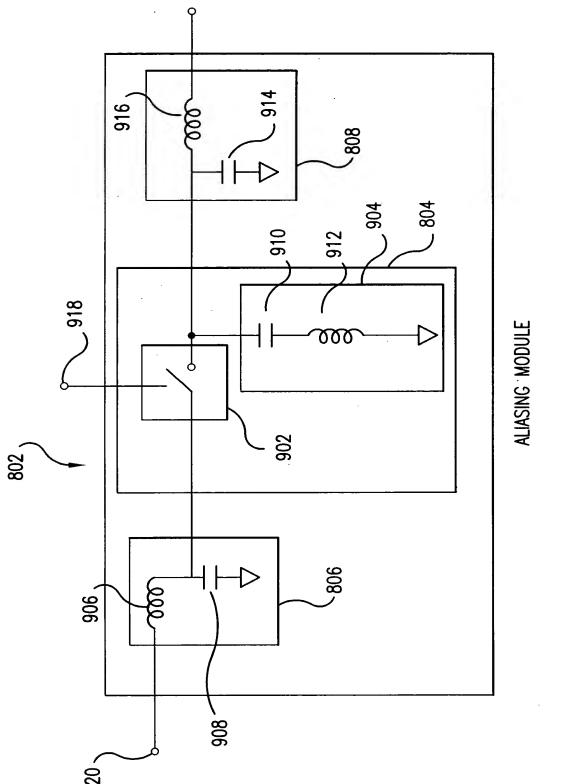


FIG 9

APR 0 3 7000 and

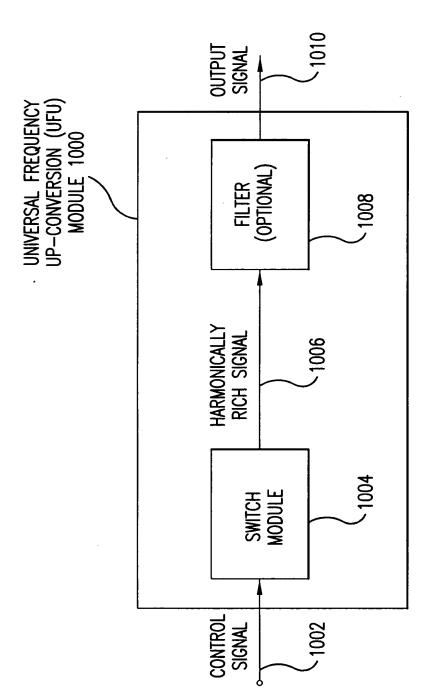
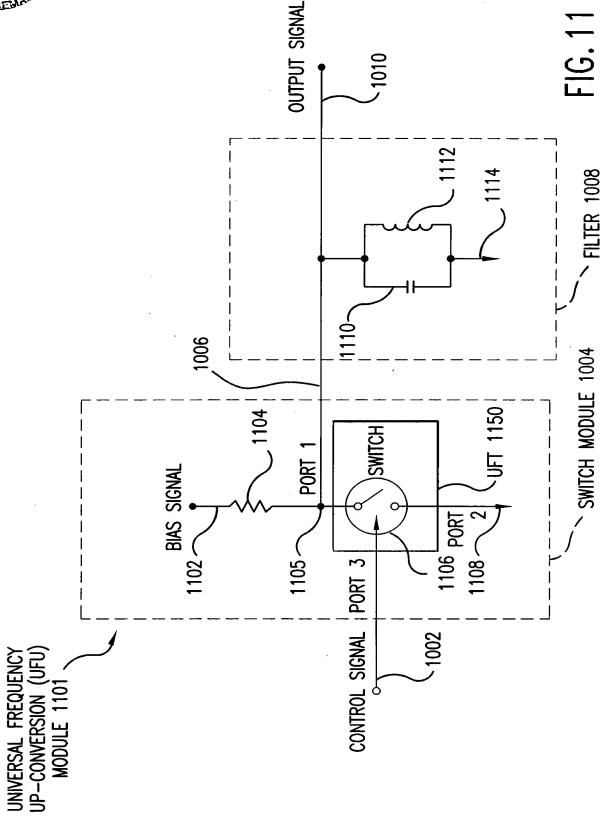


FIG. 10.





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UNIVERSAL FREQUENCY UP-CONVERSION (UFU) MODULE 1290 Appl. No. 09/986.764; Filed: November 9, 2001 Dkt. No. 1744.1330000; Group Art Unit: 2631 Inventors: Rawlins et al.; Tel: 202/371-2600 Title: Method and Apparatus for Reducing DC Offsets In a Communication System

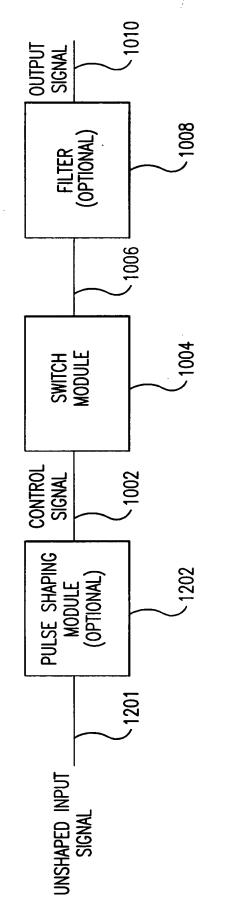
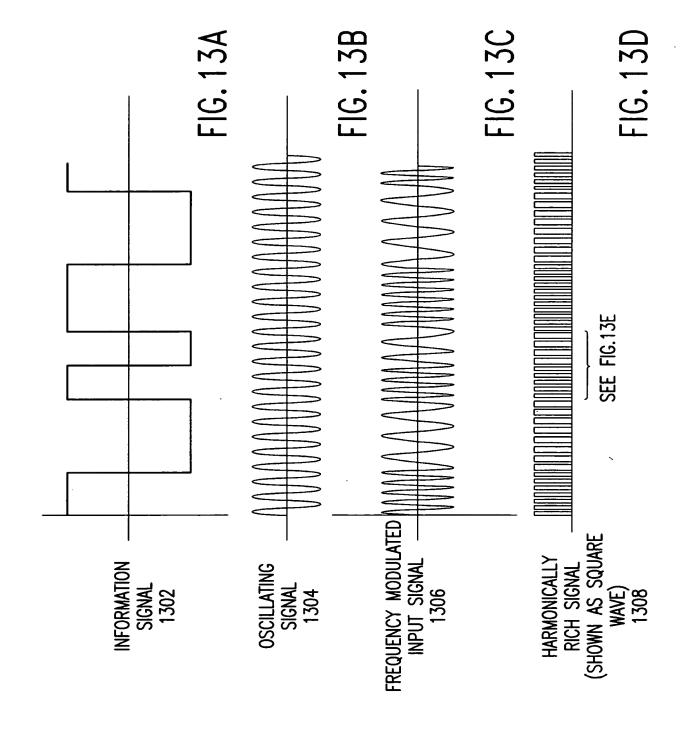
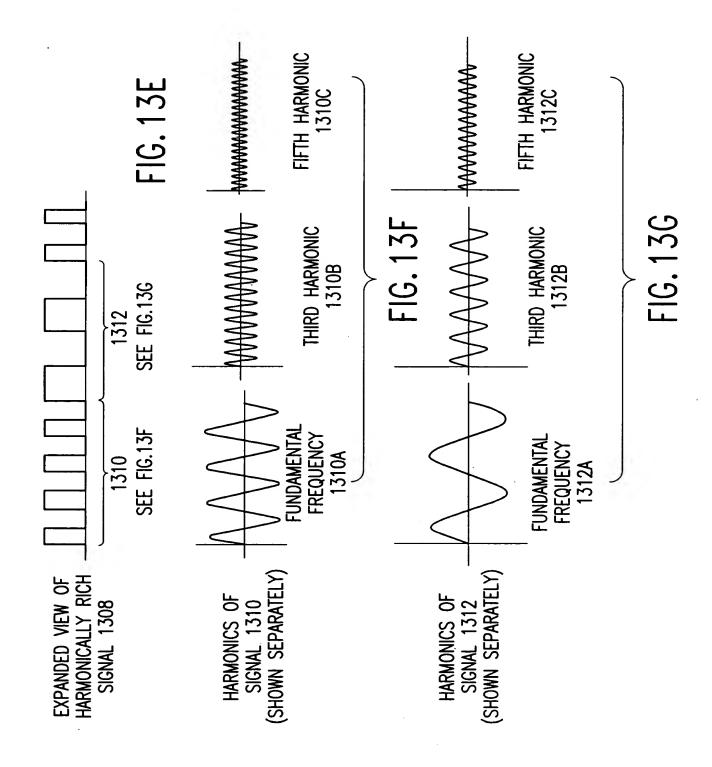


FIG. 12





APR 0 3 2000 HOLLES



APR 0 3 2000 ST

Appl. No. 09/986,764; Filed: November 9, 2001 Dkt. No. 1744.1330000; Group Art Unit: 2631 Inventors: Rawlins et al.; Tel: 202/371-2600 Title: Method and Apparatus for Reducing DC Offsets In a Communication System

HARMONICS OF SIGNALS 1310 AND 1312 (SHOWN SIMULTANEOUSLY BUT NOT SUMMED)

1310 1312 1312

FIG. 13H

13.10C) 13.12C

FILTERED OUTPUT WWW.

FIG. 131



UNIFIED DOWNCONVERTING AND FILTERING (UDF) MODULE 1402

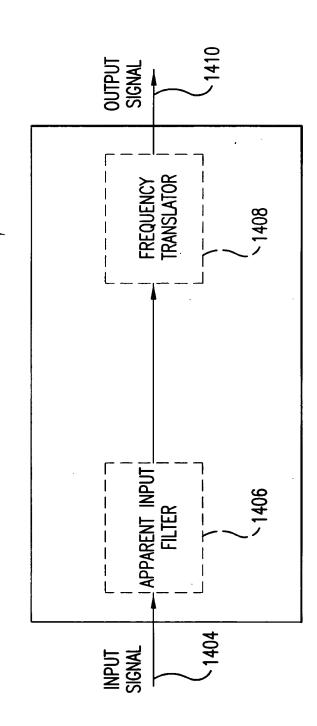
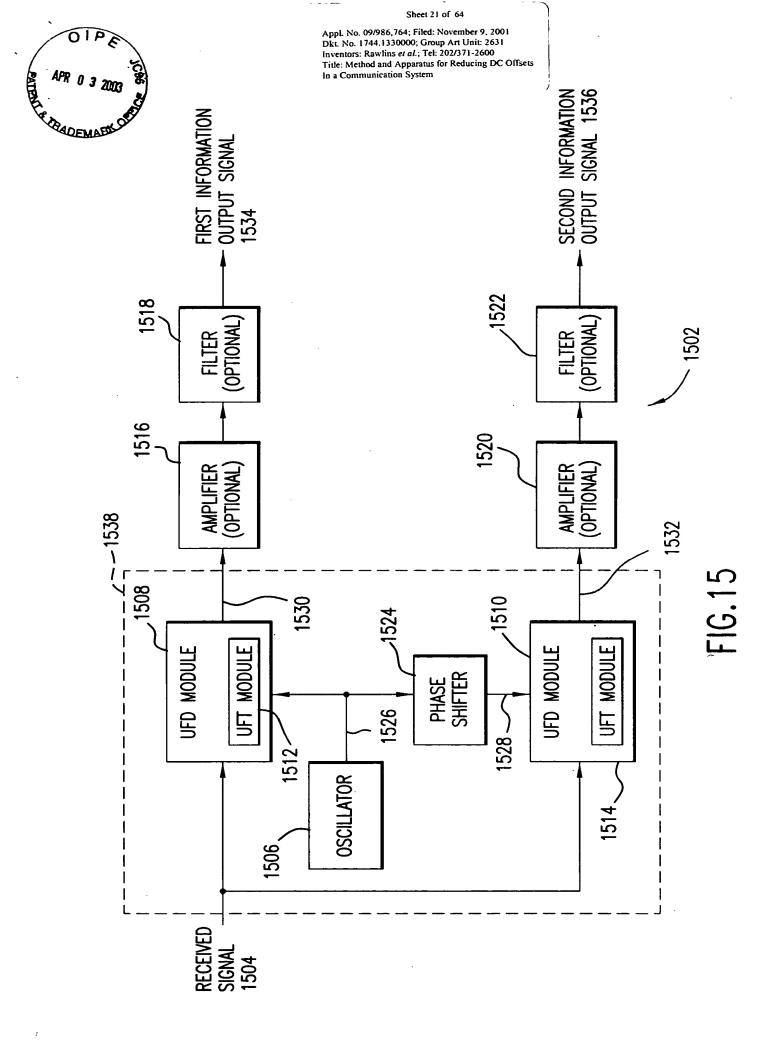
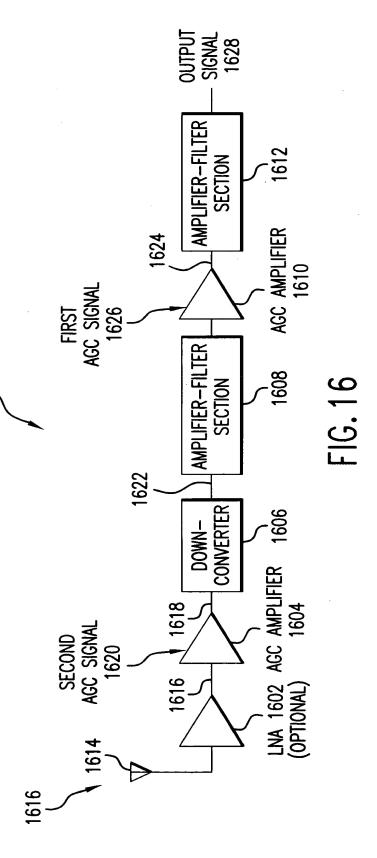


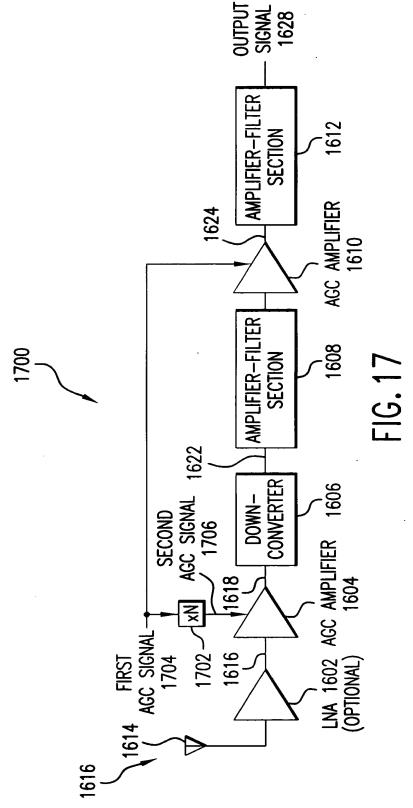
FIG. 14



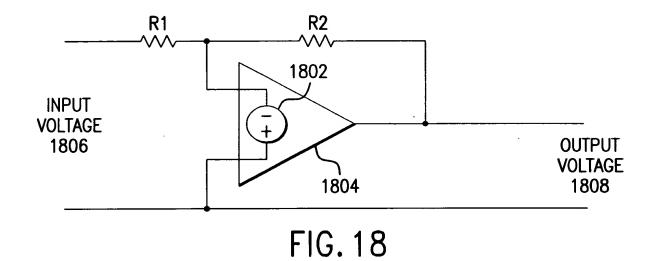


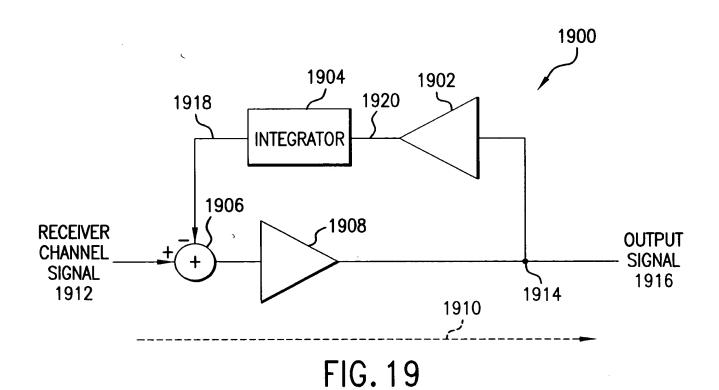














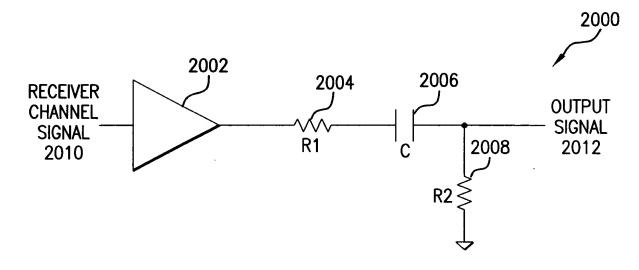


FIG. 20

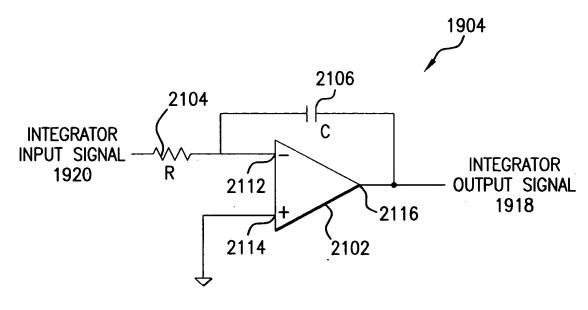
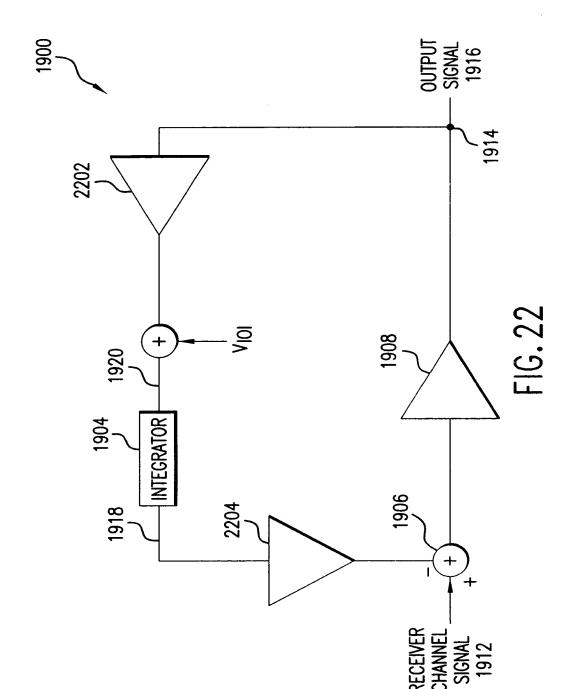
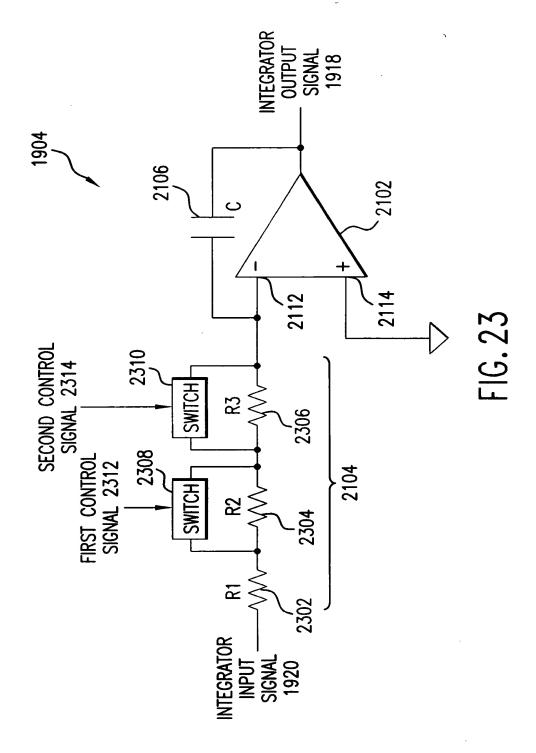


FIG. 21





APR 0 3 TOTAL CHARLES





In a Communication System

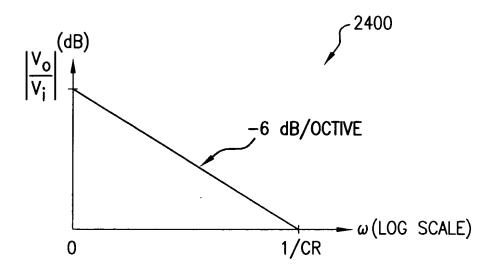


FIG. 24A

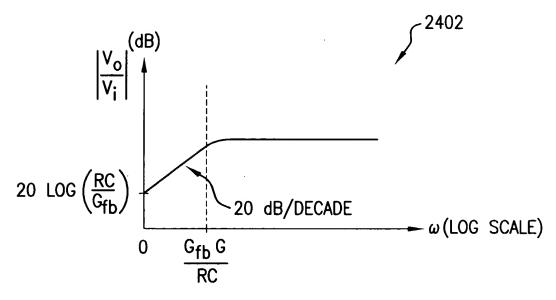


FIG. 24B



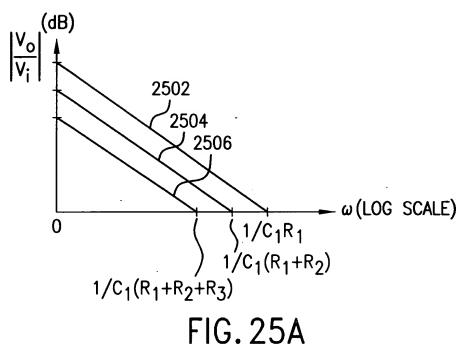


FIG. 25A

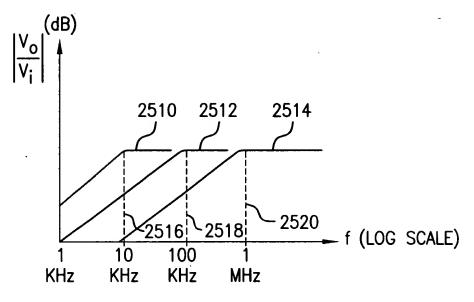


FIG. 25B



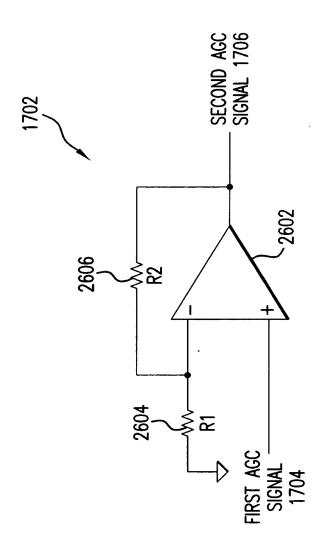


FIG. 26



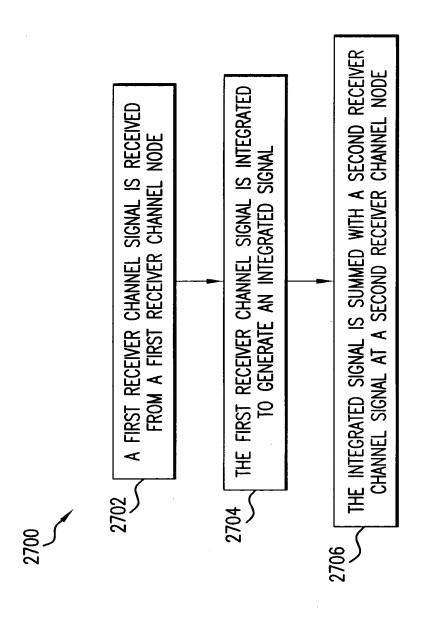
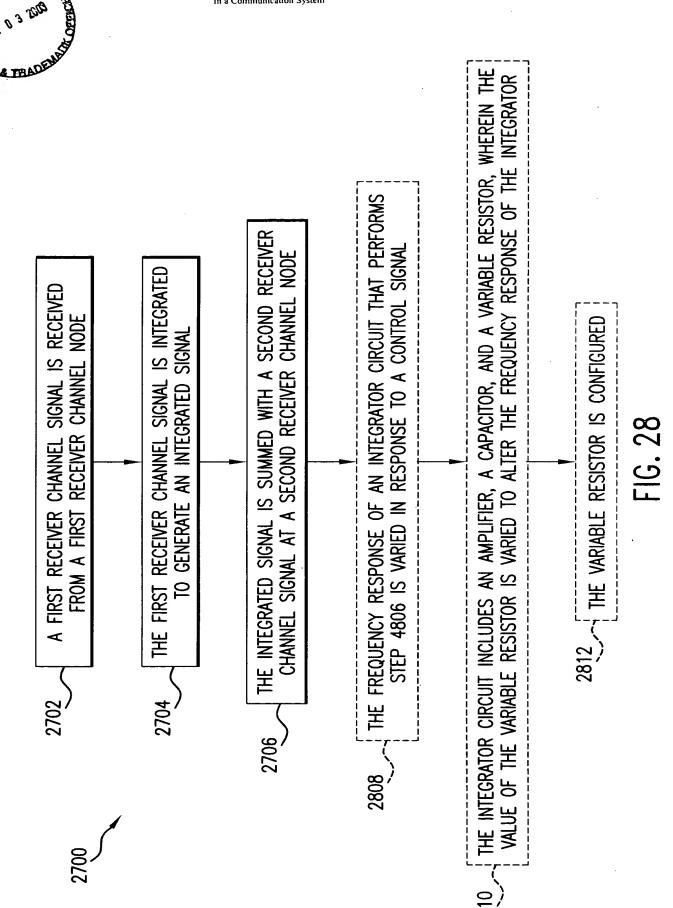


FIG. 27





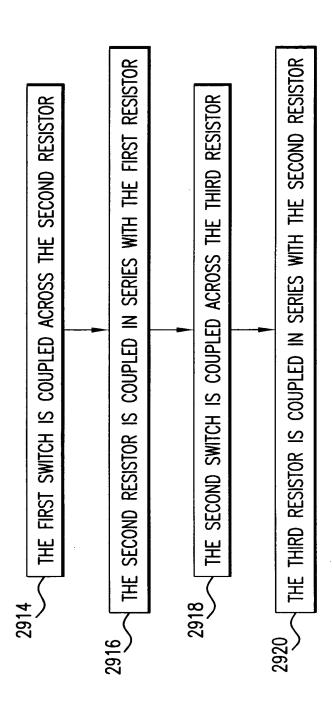
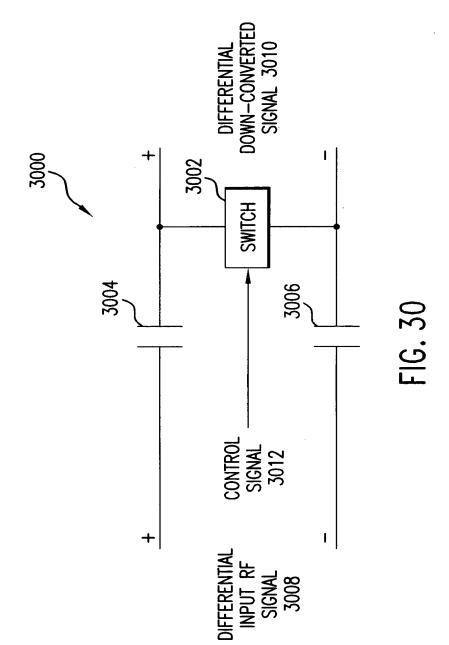
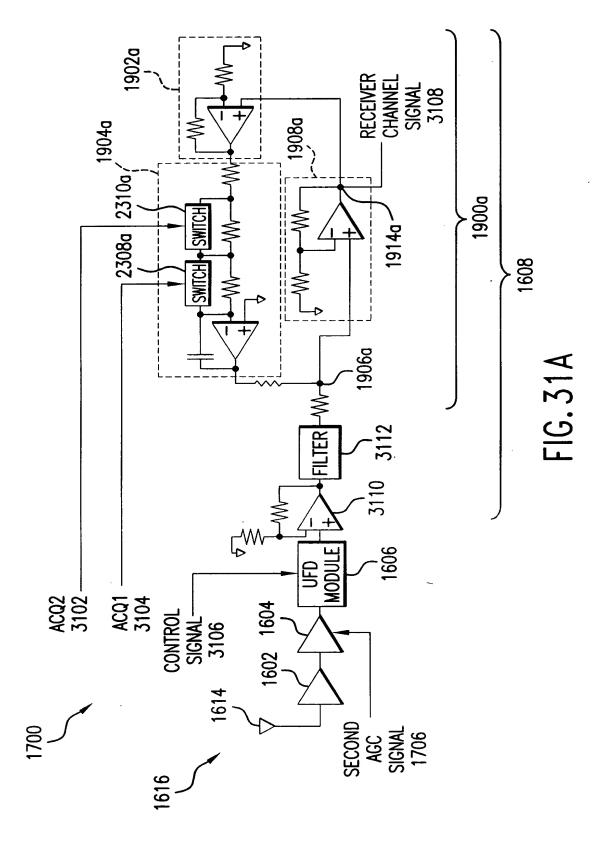


FIG. 29

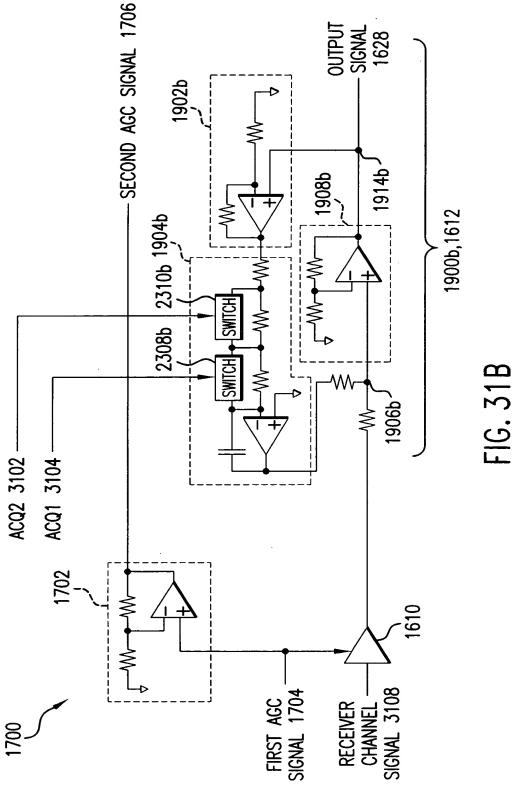








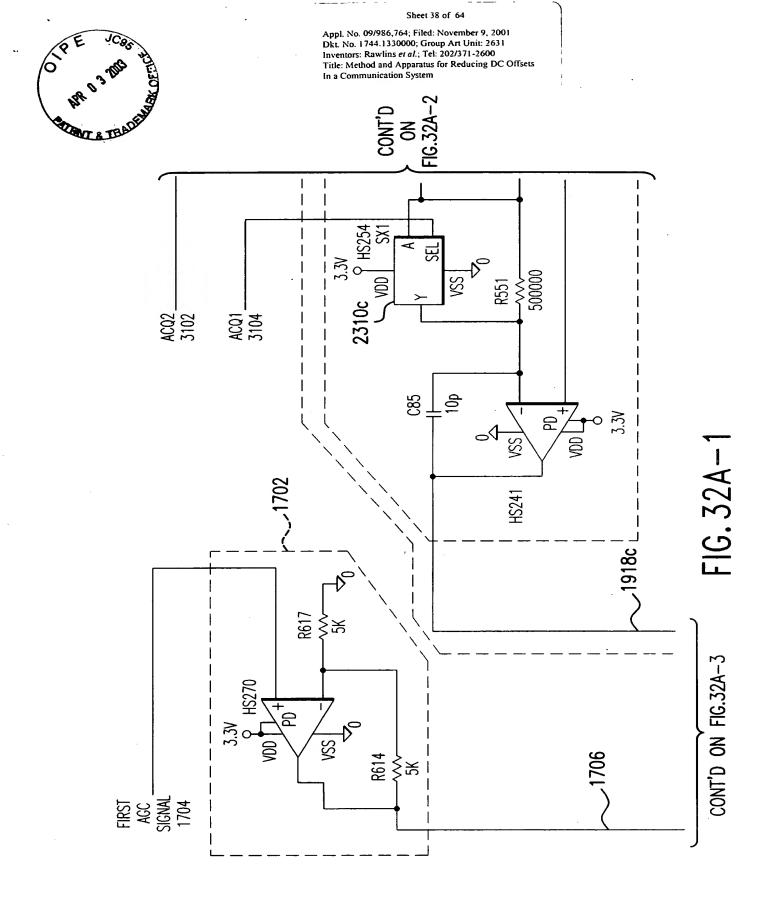






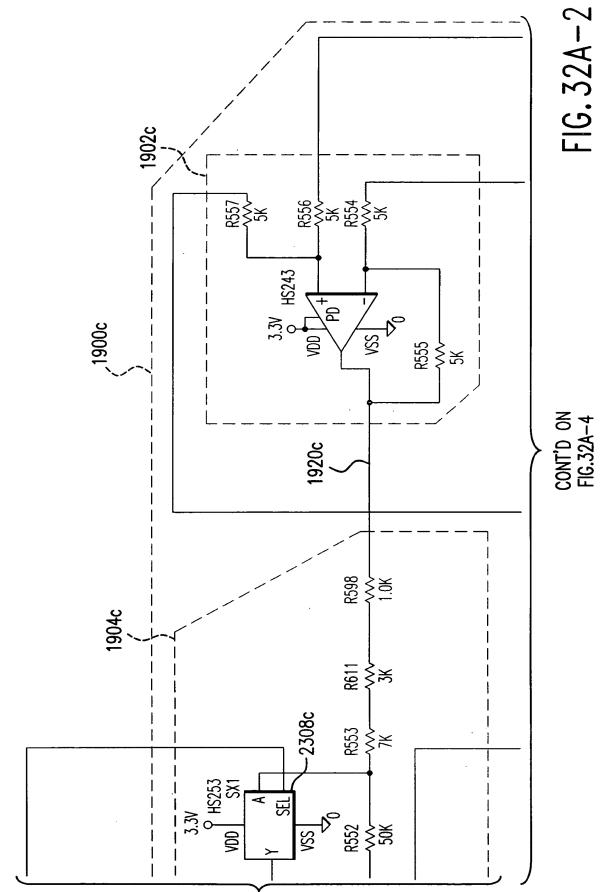
| FIG.32A-1 | FIG.32A-2 |
|-----------|-----------|
| FIG.32A-3 | FIG.32A-4 |

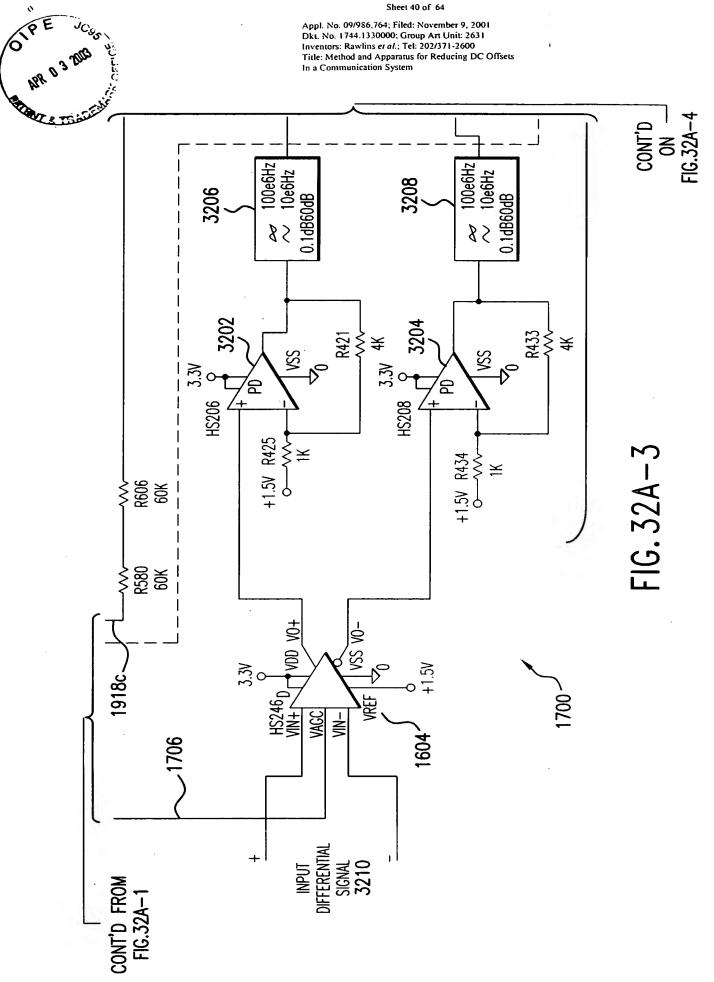
FIG. 32A

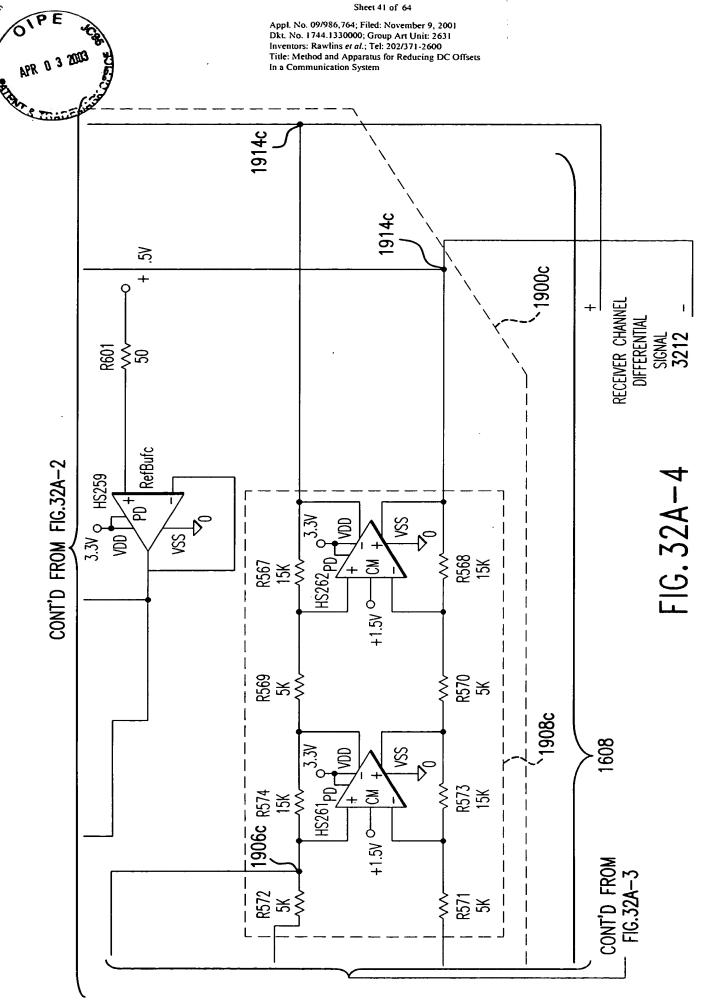


THE THATE

CONT'D FROM FIG.32A-1









| FIG.32B-1 | FIG.32B-2 | FIG.32B-3 |
|-----------|-----------|-----------|

FIG. 32B



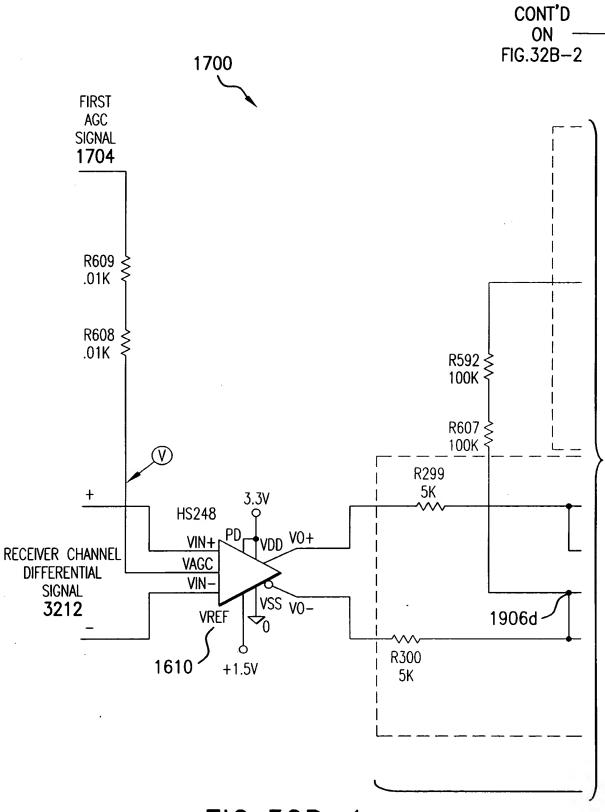


FIG. 32B-1

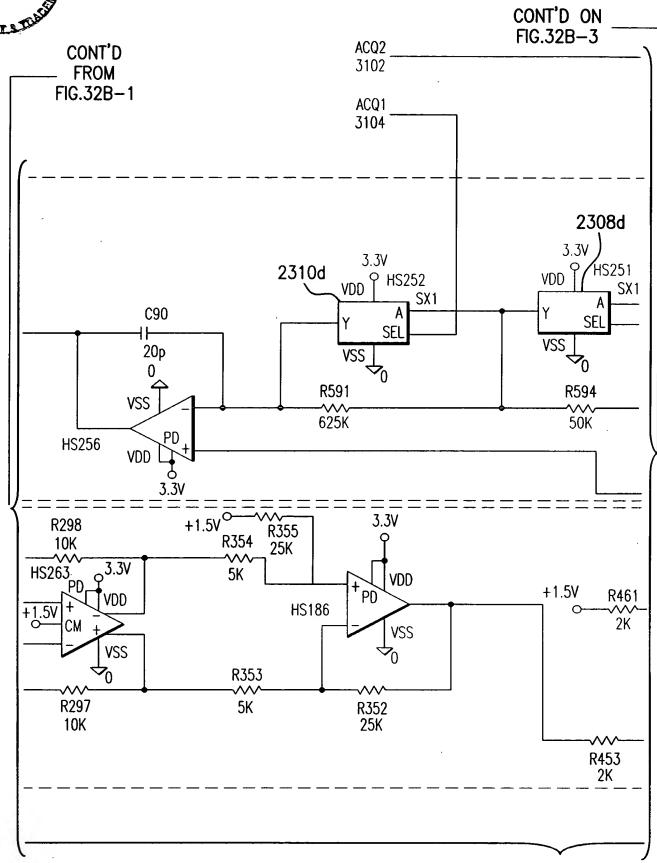


FIG. 32B-2

1900d, 1612

FIG. 32B-3



In a Communication System

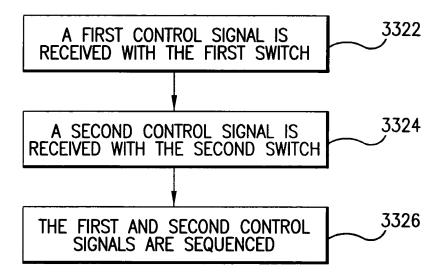


FIG. 33



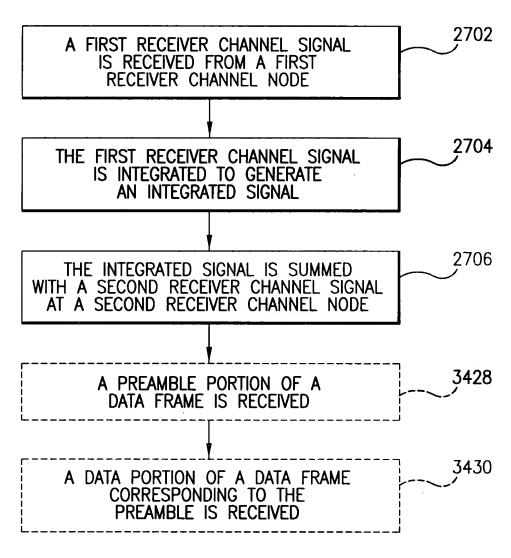
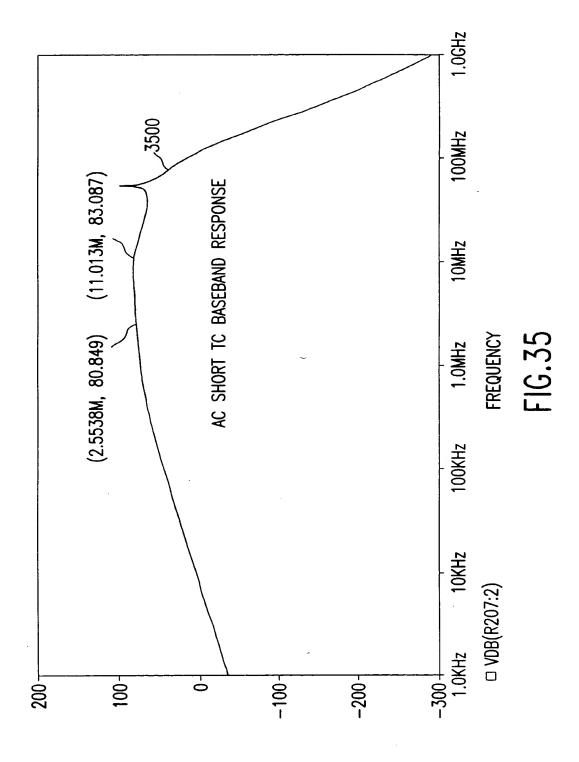
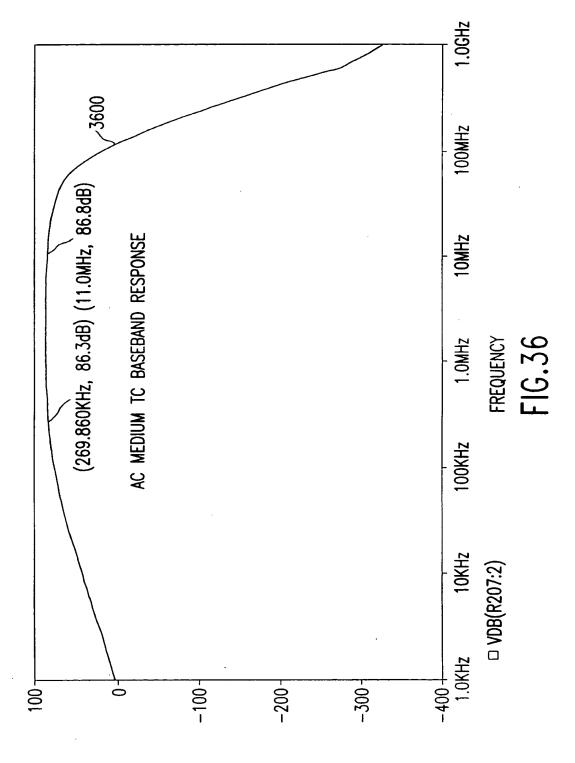


FIG. 34



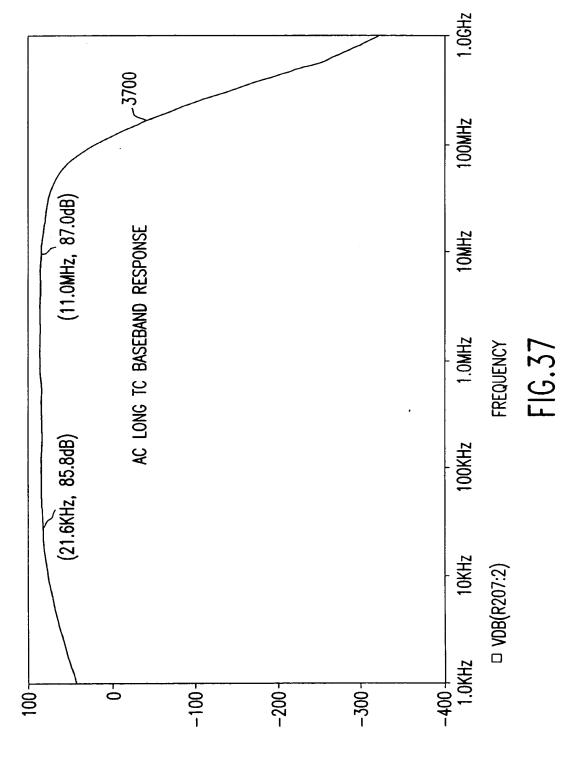




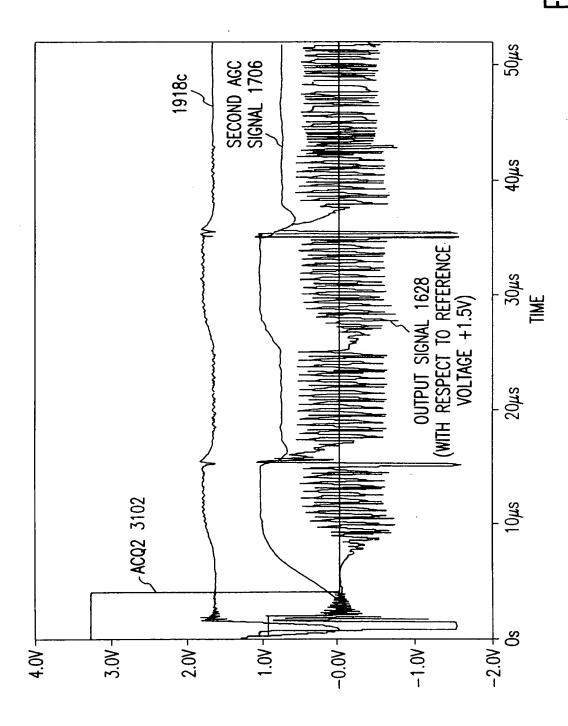


Sheet 50 of 64





16.38





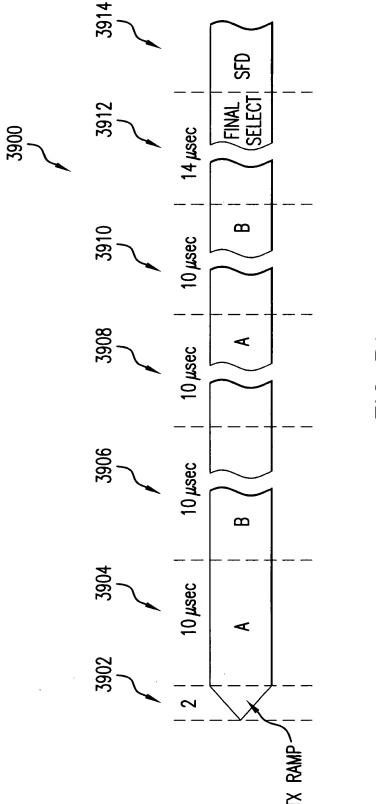


FIG. 39



 μV 4002 $+ 10^2$ 10^4 10^6 10^8 FIG. 40

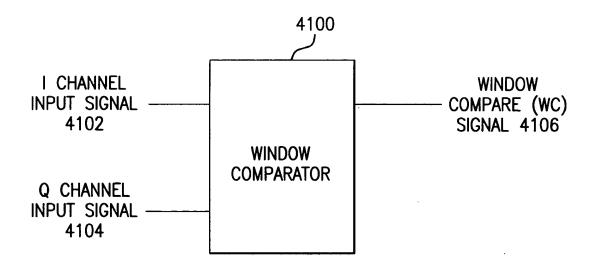
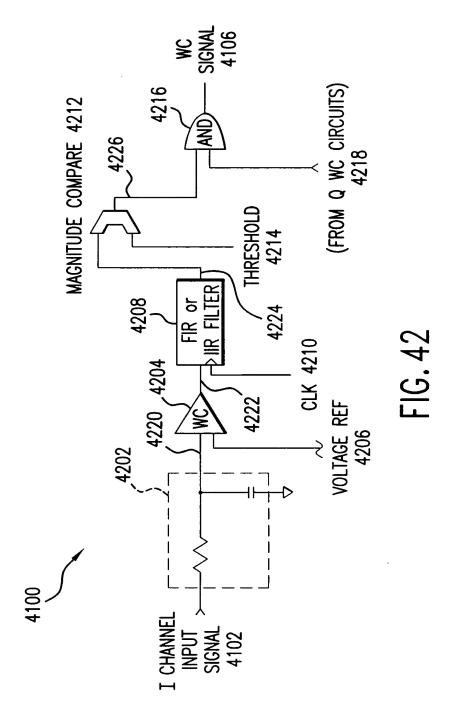


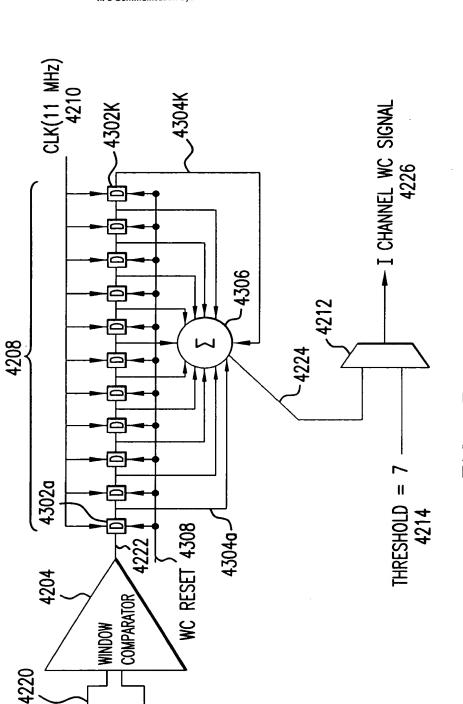
FIG. 41





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Appl. No. 09/986,764; Filed: November 9, 2001 Dkt. No. 1744.1330000; Group Art Unit: 2631 Inventors: Rawlins et al.; Tel: 202/371-2600 Title: Method and Apparatus for Reducing DC Offsets In a Communication System

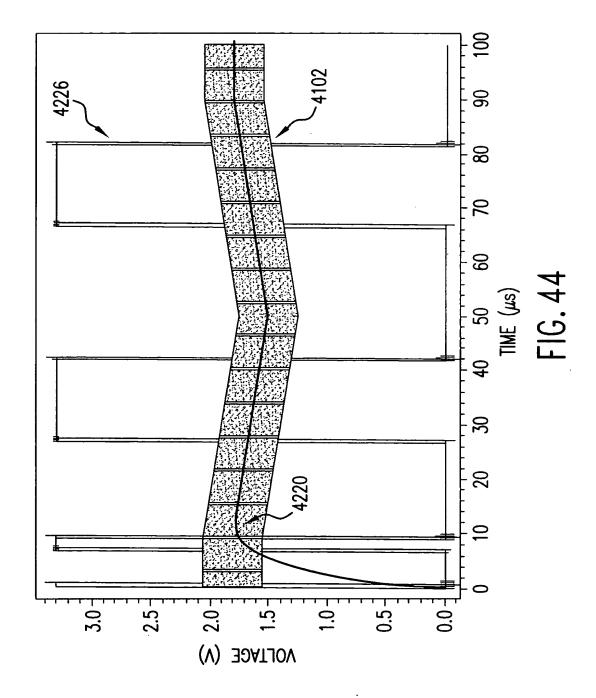


I CHANNEL INPUT + SIGNAL 4102 |

VOLTAGE -REFERENCE 4206

FIG. 43







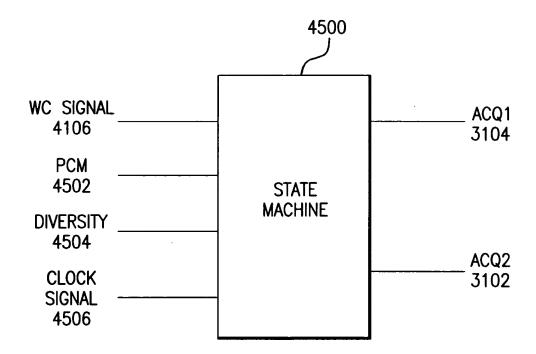
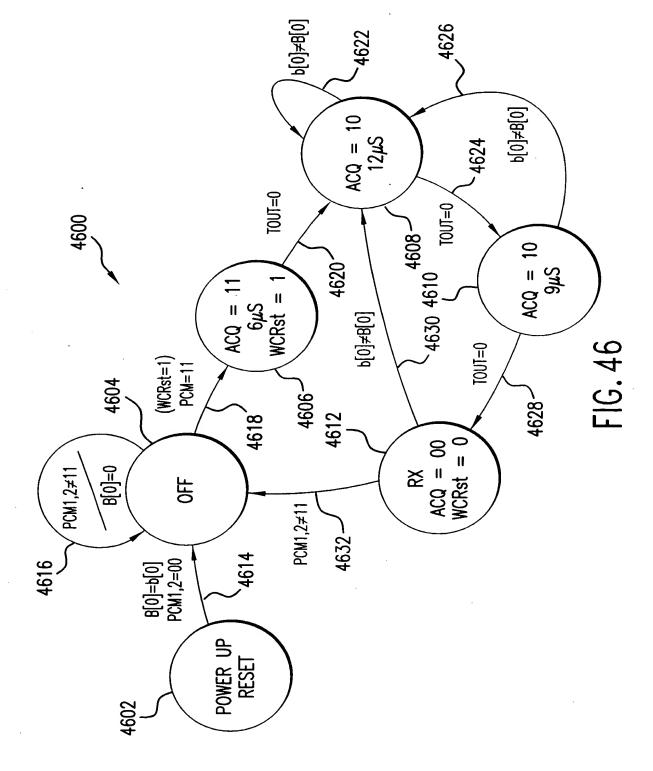


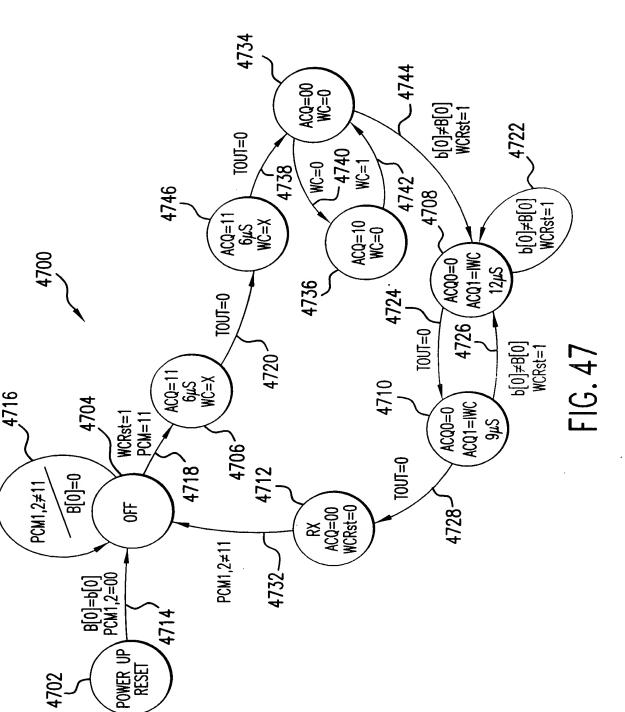
FIG. 45



In a Communication System









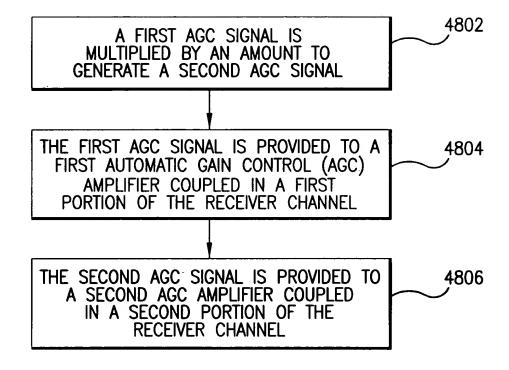


FIG. 48



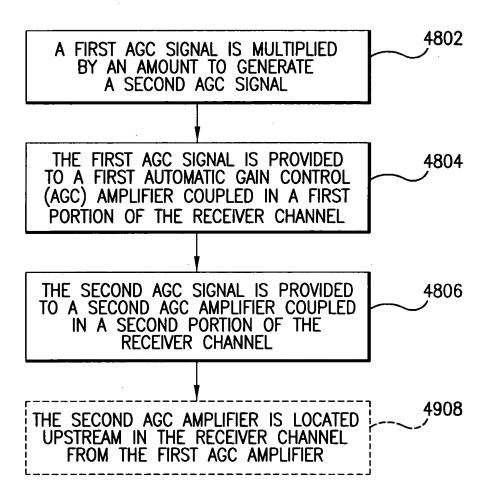


FIG. 49



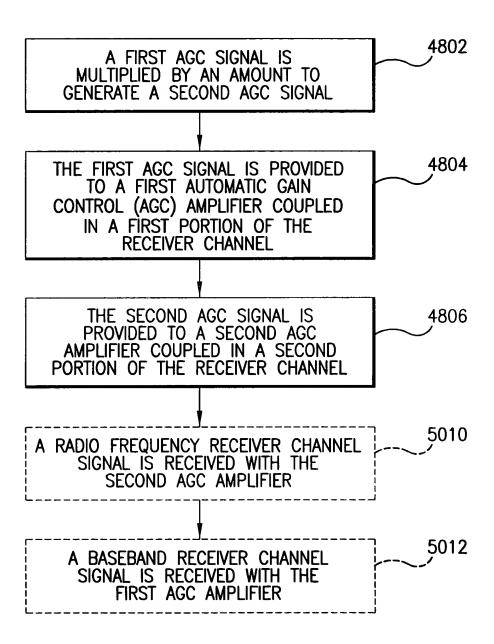


FIG. 50



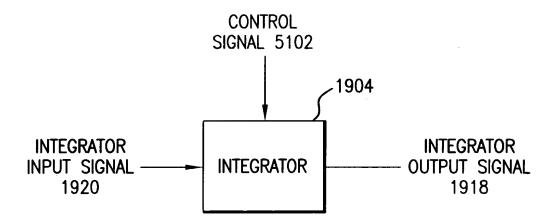


FIG. 51



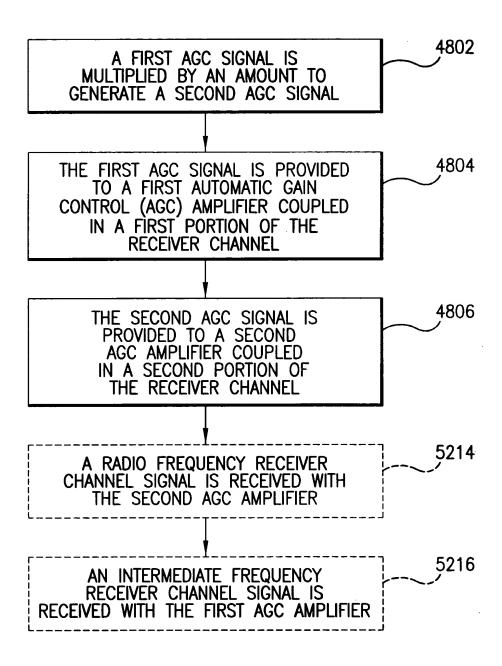


FIG. 52